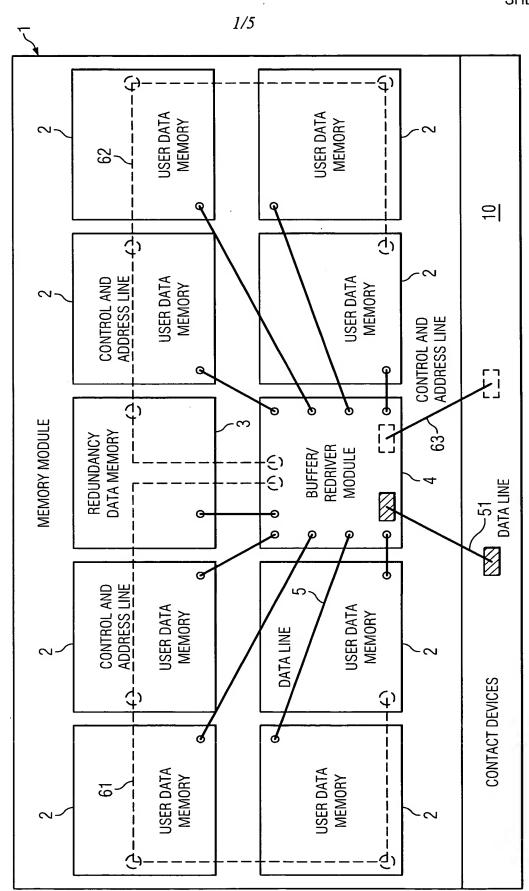
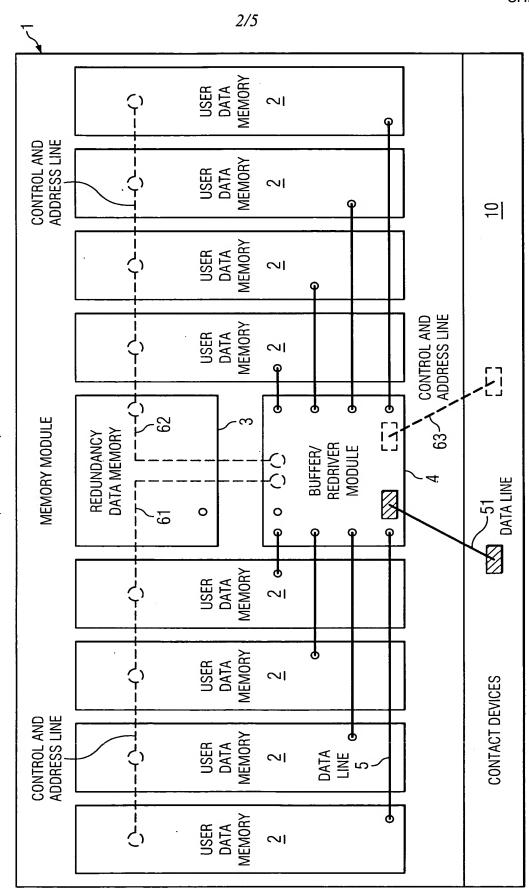


FIG. 1 (PRIOR ART)



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FIG. 2 (PRIOR ART)



3/5

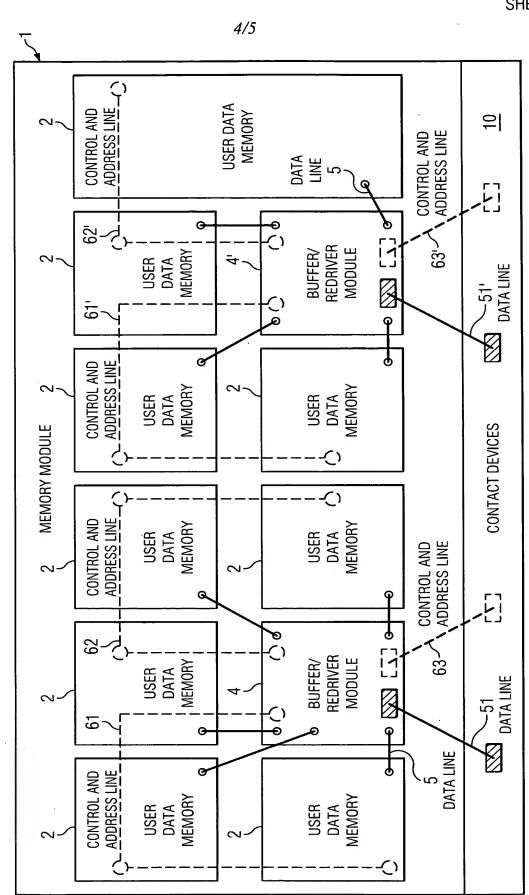


USER DATA MEMORY ر ال φ CONTROL AND ADDRESS LINE USER DATA MEMORY 21 위 USER DATA MEMORY 21 CONTROL AND ADDRESS LINE USER DATA MEMORY 21 62 MEMORY MODULE 63/ BUFFER AND ERRROR CHECKING MODULE 751 DATA LINE **β** 21 USER DATA MEMORY 61 USER DATA MEMORY **~**I CONTACT DEVICES CONTROL AND ADDRESS LINE USER DATA MEMORY DATA LINE 5 21 USER DATA MEMORY ~1

FIG. 3



FIG. 4 (PRIOR ART)





5/5 **USER DATA USER DATA** MEMORY MEMORY 위 CONTROL AND ADDRESS LINE AND ERROR 63.7 CHECKING BUFFER MODULE ノ51・ DATA LINE **USER DATA USER DATA** MEMORY MEMORY **MEMORY MODULE** CONTACT DEVICES FIG. 5 **USER DATA USER DATA** MEMORY MEMORY CONTROL AND ADDRESS LINE S BUFFER AND ERROR CHECKING MODULE 63/ DATA LINE DATA LINE -51 61 C ADDRESS LINE CONTROL AND USER DATA MEMORY **USER DATA** MEMORY